

FIG. 2A

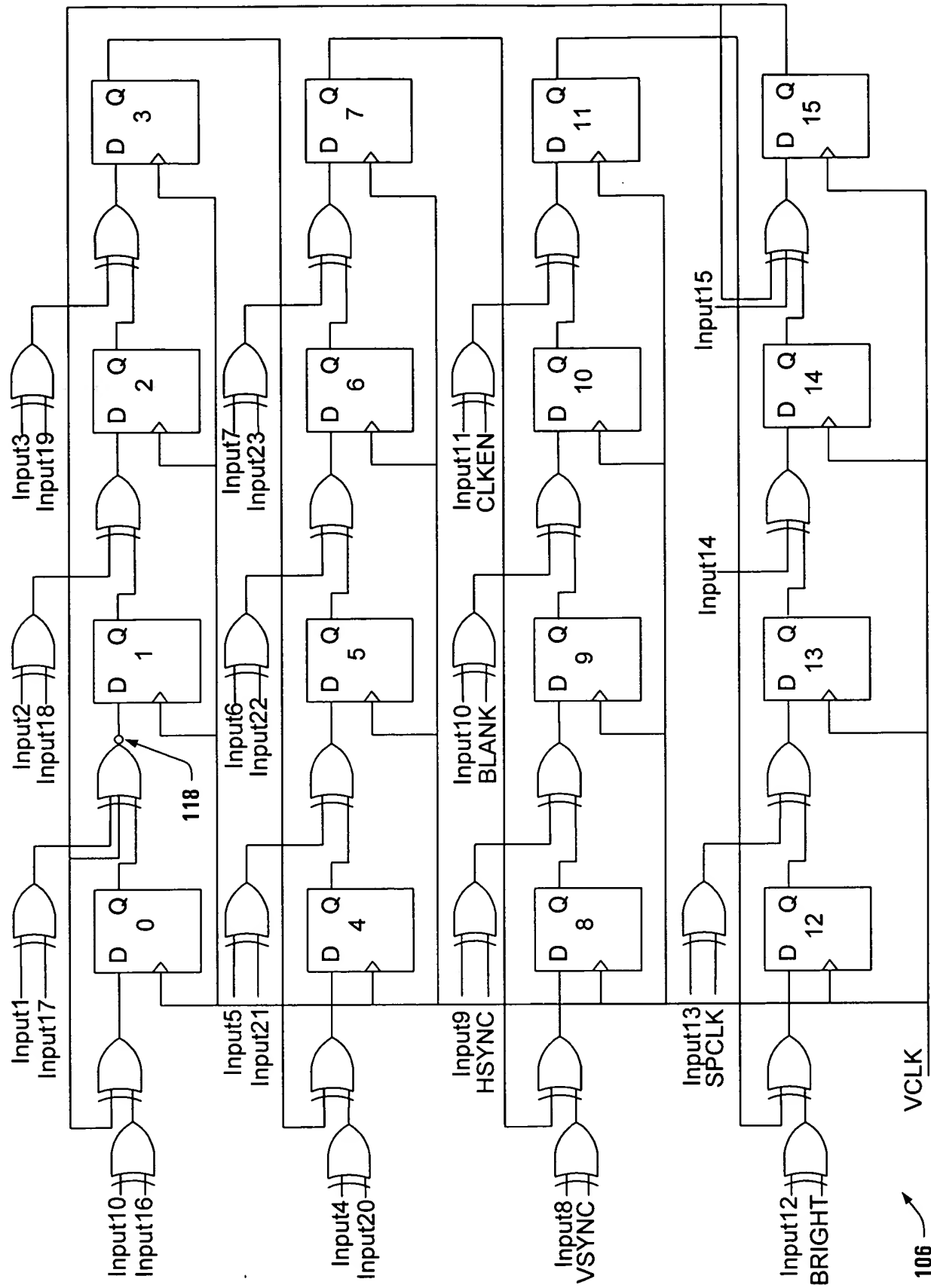


FIG. 4

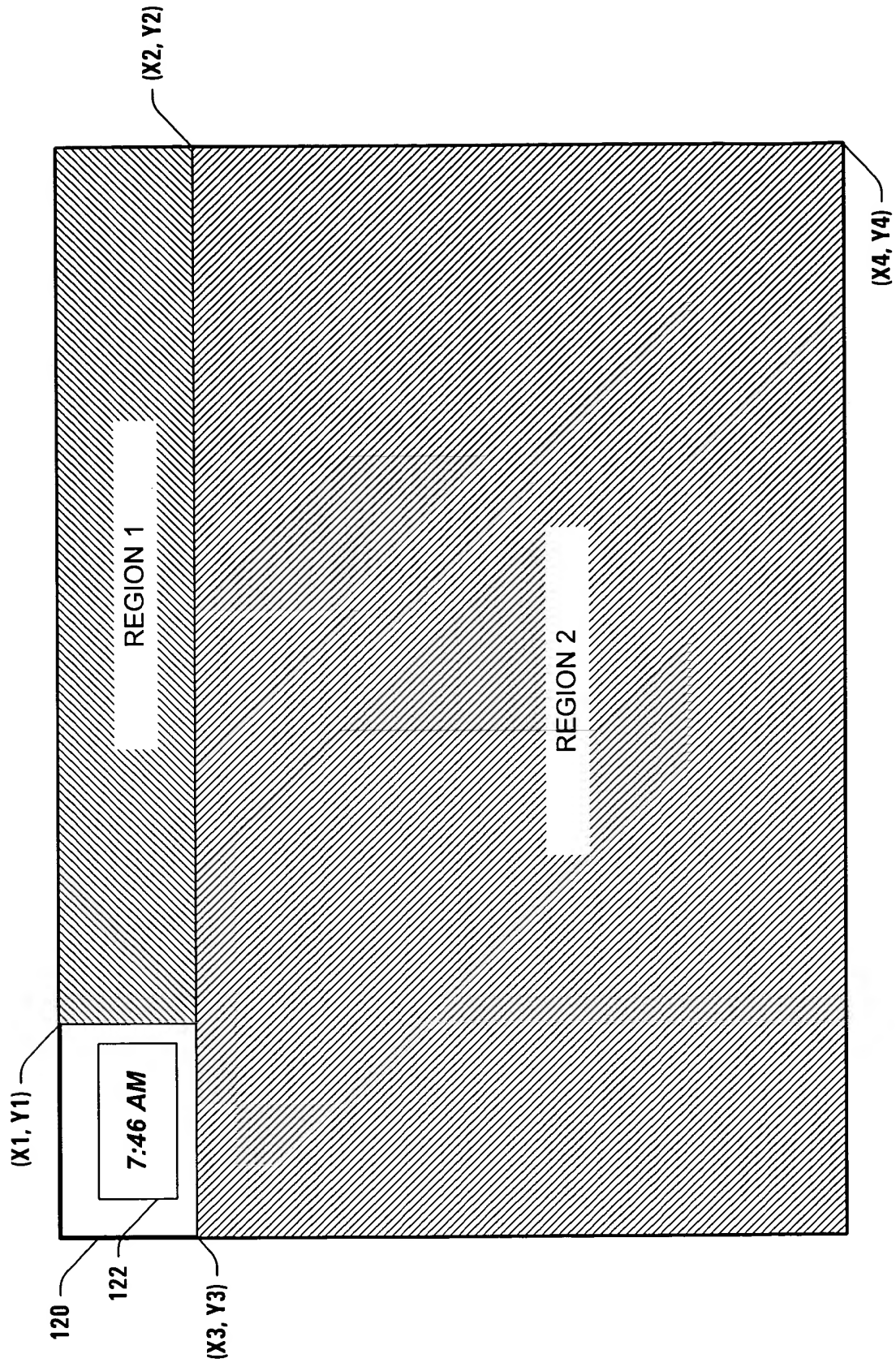


FIG. 5

31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16
RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD

15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
SIG VAL	SIG VAL	SIG VAL	SIG VAL	SIG VAL	SIG VAL	SIG VAL	SIG VAL	SIG VAL	SIG VAL	SIG VAL	SIG VAL	SIG VAL	SIG VAL	SIG VAL	SIG VAL

SIGVAL

130

FIG. 6A

31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16
EN	RSVD	SPCLK	BRIGHT	CLKEN	BLANK	HSYNC	VSYSN	PEN	PEN	PEN	PEN	PEN	PEN	PEN	PEN

15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
PEN	PEN	PEN	PEN	PEN	PEN	PEN	PEN	PEN	PEN	PEN	PEN	PEN	PEN	PEN	PEN

SIGCTL

132

FIG. 6B

31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16
RSVD	RSVD	RSVD	RSVD	RSVD	STOP ₁₀	STOP ₉	STOP ₈	STOP ₇	STOP ₆	STOP ₅	STOP ₄	STOP ₃	STOP ₂	STOP ₁	STOP ₀

15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
RSVD	RSVD	RSVD	RSVD	RSVD	START ₁₀	START ₉	START ₈	START ₇	START ₆	START ₅	START ₄	START ₃	START ₂	START ₁	START ₀

VSIGSTRTSTOP

134

FIG. 6C

160

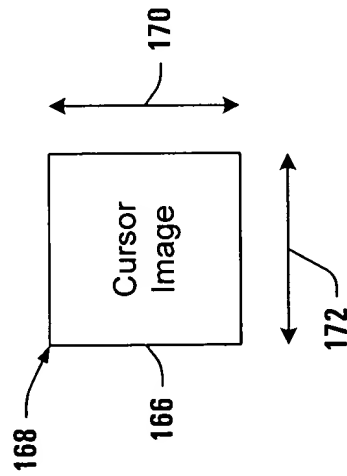
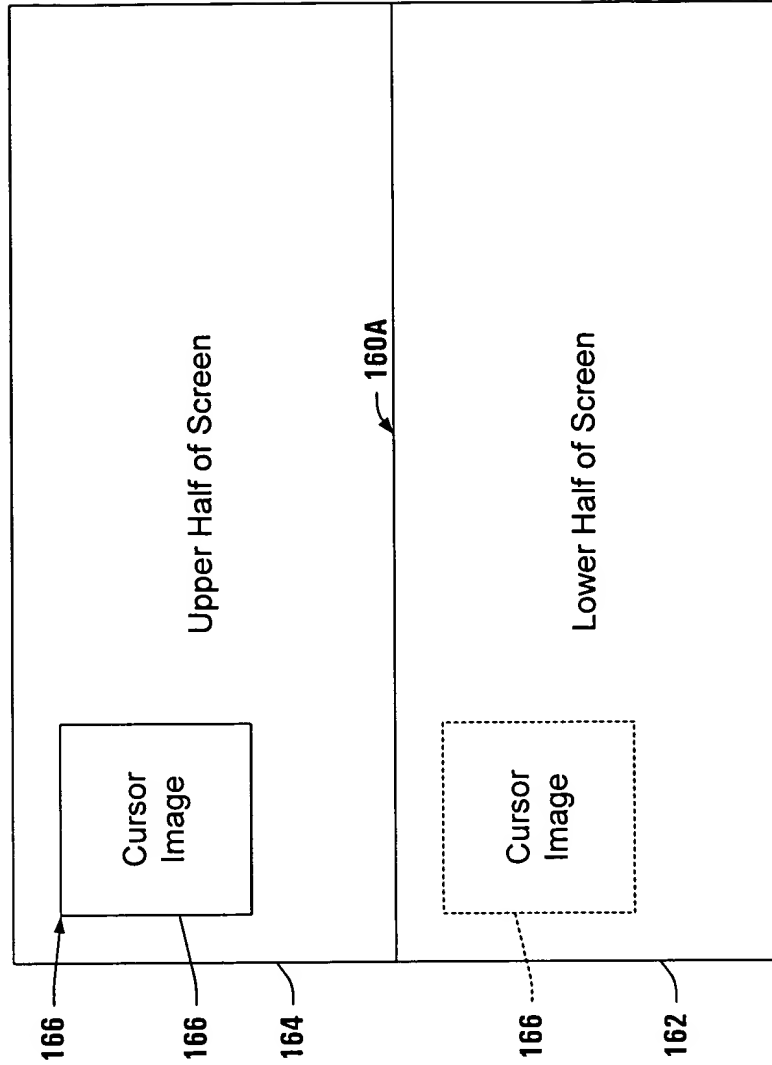


FIG. 8A

FIG. 8B

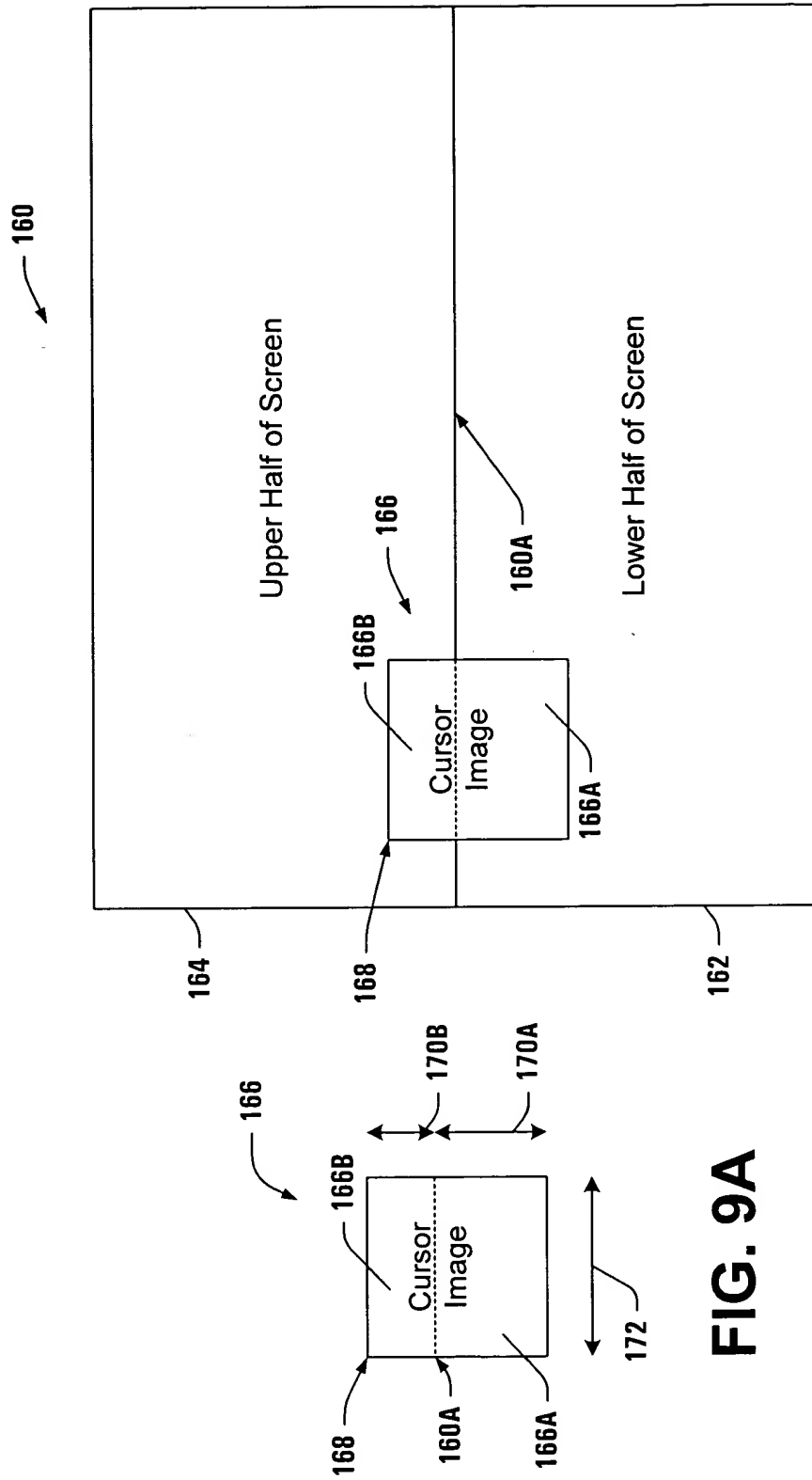


FIG. 9A

FIG. 9B

31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16
ADR	ADR	ADR	ADR	ADR	ADR	ADR	ADR	ADR	ADR	ADR	ADR	ADR	ADR	ADR	ADR

15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ADR	ADR	ADR	ADR	ADR	ADR	ADR	ADR	ADR	ADR	ADR	ADR	ADR	ADR	NA	NA

CURSOR_ADR_START

200

FIG. 11A

31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16
ADR	ADR	ADR	ADR	ADR	ADR	ADR	ADR	ADR	ADR	ADR	ADR	ADR	ADR	ADR	ADR

15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ADR	ADR	ADR	ADR	ADR	ADR	ADR	ADR	ADR	ADR	ADR	ADR	ADR	ADR	NA	NA

CURSOR_ADR_RESET

202

FIG. 11B

31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16
RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD

15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
DLNS5	DLNS4	DLNS3	DLNS2	DLNS1	DLNS0	CSTEP ₁	CSTEP ₀	CLINS5	CLINS4	CLINS3	CLINS2	CLINS1	CLINS0	CWID1	CWID0

CURSORSIZE

204

FIG. 11C

31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16
RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	COLO _R	COLO _R	COLO _R	COLO _R	COLO _R	COLO _R	COLO _R	COLO _R

15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
COLO _R	COLO _R	COLO _R	COLO _R	COLO _R	COLO _R	COLO _R	COLO _R	COLO _R	COLO _R	COLO _R	COLO _R	COLO _R	COLO _R	COLO _R	COLO _R

CURSORSIZE
CURSORCOLOR1
CURSORCOLOR2
CURSORBLINK1
CURSORBLINK2

206

FIG. 11D

31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16
RSVD	RSVD	RSVD	RSVD	RSVD	YLOC ₁₀	YLOC ₉	YLOC ₈	YLOC ₇	YLOC ₆	YLOC ₅	YLOC ₄	YLOC ₃	YLOC ₂	YLOC ₁	YLOC ₀

15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
CEN	RSVD	RSVD	RSVD	RSVD	XLOC ₁₀	XLOC ₉	XLOC ₈	XLOC ₇	XLOC ₆	XLOC ₅	XLOC ₄	XLOC ₃	XLOC ₂	XLOC ₁	XLOC ₀

CURSORXYLOC

208

FIG. 11E

31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16
RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD

15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
CLHEN	RSVD	RSVD	RSVD	RSVD	YLOC ₁₀	YLOC ₉	YLOC ₈	YLOC ₇	YLOC ₆	YLOC ₅	YLOC ₄	YLOC ₃	YLOC ₂	YLOC ₁	YLOC ₀

CURSOR_DHSCAN_LH_YLOC

210

FIG. 11F

31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16
RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD

15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	EN	RATE	RATE	RATE	RATE	RATE	RATE	RATE	RATE

CURSORBLINK

212 ↗

FIG. 11G

31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16
RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	ESTR T ₃	ESTR T ₂	ESTR T ₁	ESTR T ₀	CNT3	CNT2	CNT1	CNT0

15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	DAT	DAT	DAT	DAT	DAT	DAT	DAT	DAT

PARLLIFIN

234 →

FIG. 13C

FIG. 14A

FIG. 14A

- These bits are an ORed combination of the bit value shown and the next significant bit below (This rounds the color value to nearest color).
- These bits do not get a substitute and are defined to the values controlled by the pixel output mode in the upper part of the table.
- These bits are pinned out in certain variants only.
- Set PIXELMODE.P19511 high to use these pins as outputs.

FIG. 14B

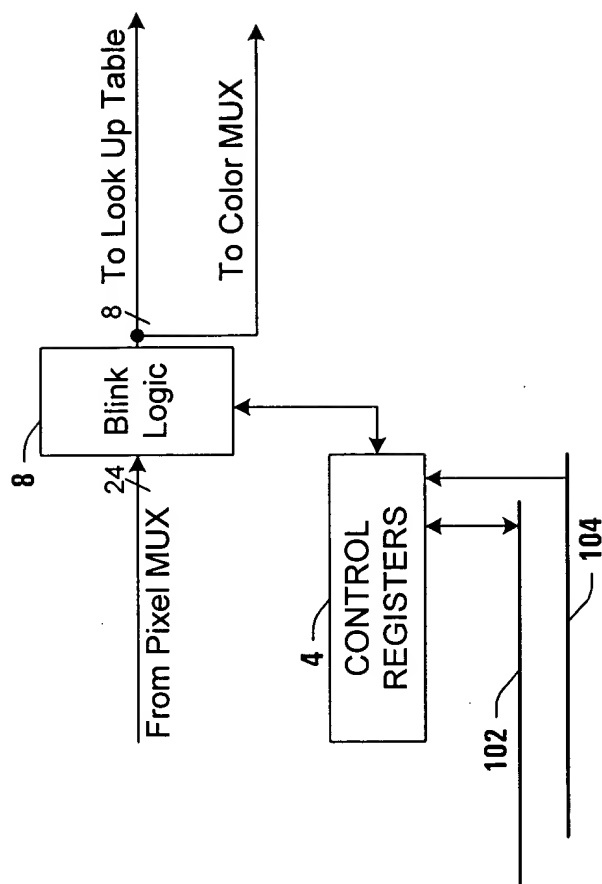


FIG. 15

31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16
RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD

15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RATE	RATE	RATE	RATE	RATE	RATE	RATE	RATE

BLINKRATE

250

FIG. 16A

31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16
RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	MASK	MASK	MASK	MASK	MASK	MASK	MASK	MASK

15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
MASK	MASK	MASK	MASK	MASK	MASK	MASK	MASK	MASK	MASK	MASK	MASK	MASK	MASK	MASK	MASK

BLINKMASK

252

FIG. 16B

	1980	1981	1982	1983	1984	1985	1986	1987	1988	1989	1990	1991	1992	1993	1994	1995	1996	1997	1998	1999	2000	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012	2013	2014	2015	2016	2017	2018	2019	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033	2034	2035	2036	2037	2038	2039	2040	2041	2042	2043	2044	2045	2046	2047	2048	2049	2050	2051	2052	2053	2054	2055	2056	2057	2058	2059	2060	2061	2062	2063	2064	2065	2066	2067	2068	2069	2070	2071	2072	2073	2074	2075	2076	2077	2078	2079	2080	2081	2082	2083	2084	2085	2086	2087	2088	2089	2090	2091	2092	2093	2094	2095	2096	2097	2098	2099	2100	2101	2102	2103	2104	2105	2106	2107	2108	2109	2110	2111	2112	2113	2114	2115	2116	2117	2118	2119	2120	2121	2122	2123	2124	2125	2126	2127	2128	2129	2130	2131	2132	2133	2134	2135	2136	2137	2138	2139	2140	2141	2142	2143	2144	2145	2146	2147	2148	2149	2150	2151	2152	2153	2154	2155	2156	2157	2158	2159	2160	2161	2162	2163	2164	2165	2166	2167	2168	2169	2170	2171	2172	2173	2174	2175	2176	2177	2178	2179	2180	2181	2182	2183	2184	2185	2186	2187	2188	2189	2190	2191	2192	2193	2194	2195	2196	2197	2198	2199	2200	2201	2202	2203	2204	2205	2206	2207	2208	2209	2210	2211	2212	2213	2214	2215	2216	2217	2218	2219	2220	2221	2222	2223	2224	2225	2226	2227	2228	2229	2230	2231	2232	2233	2234	2235	2236	2237	2238	2239	2240	2241	2242	2243	2244	2245	2246	2247	2248	2249	2250	2251	2252	2253	2254	2255	2256	2257	2258	2259	2260	2261	2262	2263	2264	2265	2266	2267	2268	2269	2270	2271	2272	2273	2274	2275	2276	2277	2278	2279	2280	2281	2282	2283	2284	2285	2286	2287	2288	2289	2290	2291	2292	2293	2294	2295	2296	2297	2298	2299	2300	2301	2302	2303	2304	2305	2306	2307	2308	2309	2310	2311	2312	2313	2314	2315	2316	2317	2318	2319	2320	2321	2322	2323	2324	2325	2326	2327	2328	2329	2330	2331	2332	2333	2334	2335	2336	2337	2338	2339	2340	2341	2342	2343	2344	2345	2346	2347	2348	2349	2350	2351	2352	2353	2354	2355	2356	2357	2358	2359	2360	2361	2362	2363	2364	2365	2366	2367	2368	2369	2370	2371	2372	2373	2374	2375	2376	2377	2378	2379	2380	2381	2382	2383	2384	2385	2386	2387	2388	2389	2390	2391	2392	2393	2394	2395	2396	2397	2398	2399	2400	2401	2402	2403	2404	2405	2406	2407	2408	2409	2410	2411	2412	2413	2414	2415	2416	2417	2418	2419	2420	2421	2422	2423	2424	2425	2426	2427	2428	2429	2430	2431	2432	2
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[illegible][illegible]

BLINKPATRN

FIG. 16C

254

31	RSVD
30	RSVD
29	RSVD
28	RSVD
27	RSVD
26	RSVD
25	RSVD
24	RSVD
23	P MASK
22	P MASK
21	P MASK
20	P MASK
19	P MASK
18	P MASK
17	P MASK
16	P MASK

[illegible]

PATTERNMASK

FIG. 16D

256 ↗

[illegible][illegible][illegible]

BG_OFFSET

258 ↗

FIG. 16E

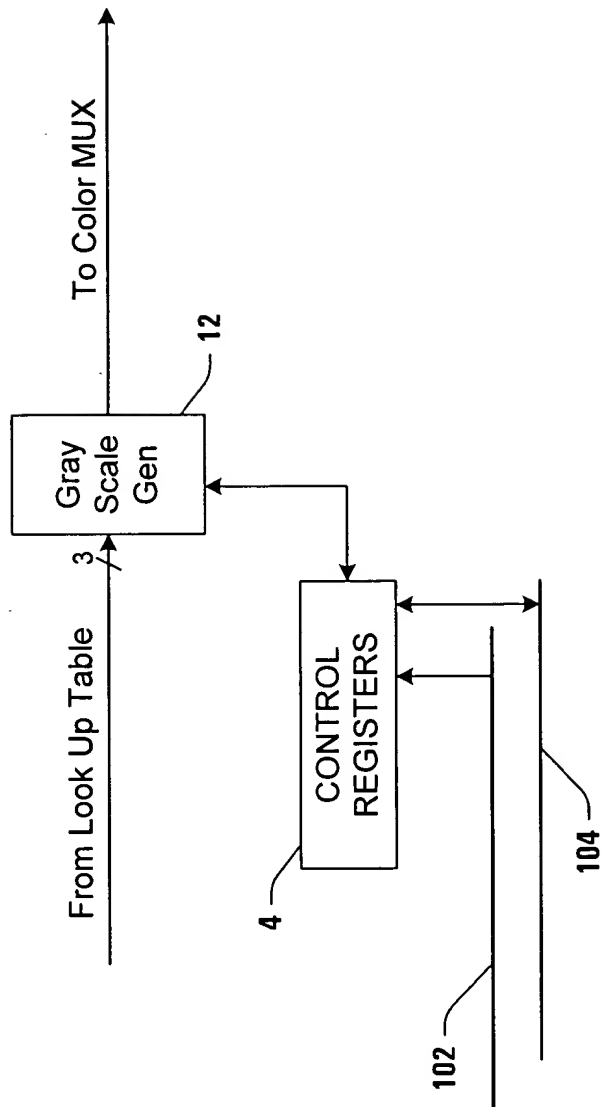


FIG. 17

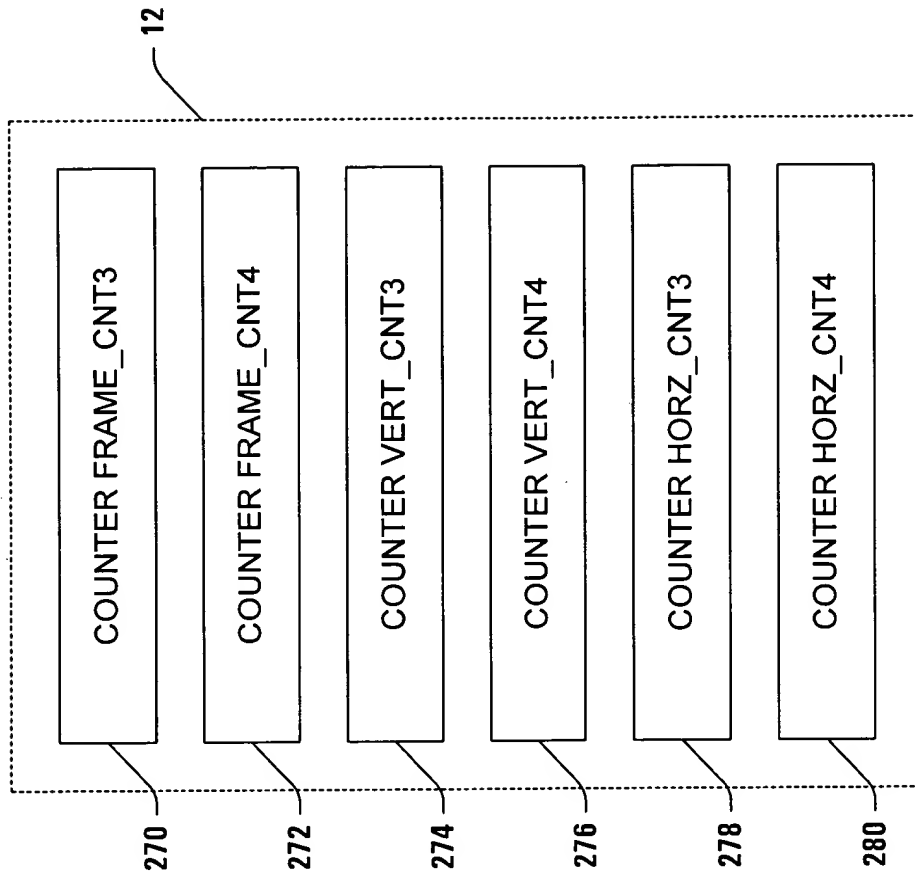


FIG. 18

31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16
RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	FRAME	VERT	HORZ
15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
D15	D14	D13	D12	D11	D10	D9	D8	D7	D6	D5	D4	D3	D2	D1	D0


282 

FIG. 19

F.V.H def for pixel in value	FRAME Ctr	Vert Ctr	Horz Ctr	VCNT (lines) HCNT (pixels) register address	11	11	11	11	10	01	11	10	10	10	01	10	01	10	01	01	01	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
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FIG. 20

FRAME		Vert	Horz	VCNT (lines)		11	11	11	11	10	10	10	10	10	01	01	01	01	01	01	00	00	00	00	00	GSLUT Address *4	
Ctr		Ctr		HCNT (pixels)		11	11	10	01	00	11	10	01	00	11	01	01	01	01	01	00	11	10	01	00	FRAME	Pixel
D18	D17	D16	register address		b15	b14	b13	b12	b11	b10	b09	b08	b07	b06	b05	b04	b03	b02	b01	b00							
X	X	X	base + 0x80		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00	000	
			base + 0xA0		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	01	000	
			base + 0xC0		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	10	000	
			base + 0xE0		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	11	000	
X	X	X	base + 0x9C		1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	00	111	
			base + 0xBC		1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	01	111	
			base + 0xDC		1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	10	111	
			base + 0xFC		1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	11	111	

302

FIG. 21

304 →

	H	O	R	Z
FRAME 0				
V	1	1	1	1
E	1	1	1	1
R	1	1	1	1
T	1	1	1	1

FRAME 1				
0	0	0	0	0
0	0	0	0	0
0	0	0	0	0
0	0	0	0	0

FRAME 2				
1	1	1	1	1
1	1	1	1	1
1	1	1	1	1
1	1	1	1	1

FRAME 3				
0	0	0	0	0
0	0	0	0	0
0	0	0	0	0
0	0	0	0	0

FIG. 22

FRAME	Vert	Horz	VCNT (lines)	11	11	11	11	11	10	10	10	10	10	01	01	01	01	00	00	00	00	00	00	GSLUT Address *4
Ctr	Dtr	Ctr	HCNT (pixels)	11	10	01	00	11	10	01	00	11	00	11	01	01	00	11	10	01	00	00	00	Pixel
D18	D17	D16	register address	015	014	013	012	011	010	009	008	007	006	005	004	003	002	001	000	000	000	000	000	Value
1	1	1	base + 0x8C	0	1	0	1	1	1	0	0	0	1	0	1	0	1	0	0	1	0	0	0	011
			base + 0xAC	1	0	1	0	0	0	1	1	1	1	0	1	0	1	0	0	1	0	0	0	011
			base + 0xCC	1	1	0	0	0	1	0	1	0	1	1	0	1	0	1	0	1	0	1	0	011
			base + 0xEC	0	0	1	1	1	1	0	1	0	1	1	0	0	1	0	1	0	1	0	1	011

310

FIG. 25

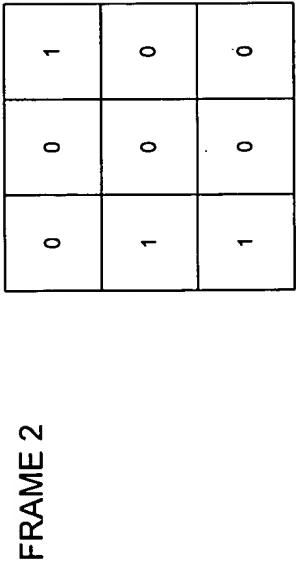
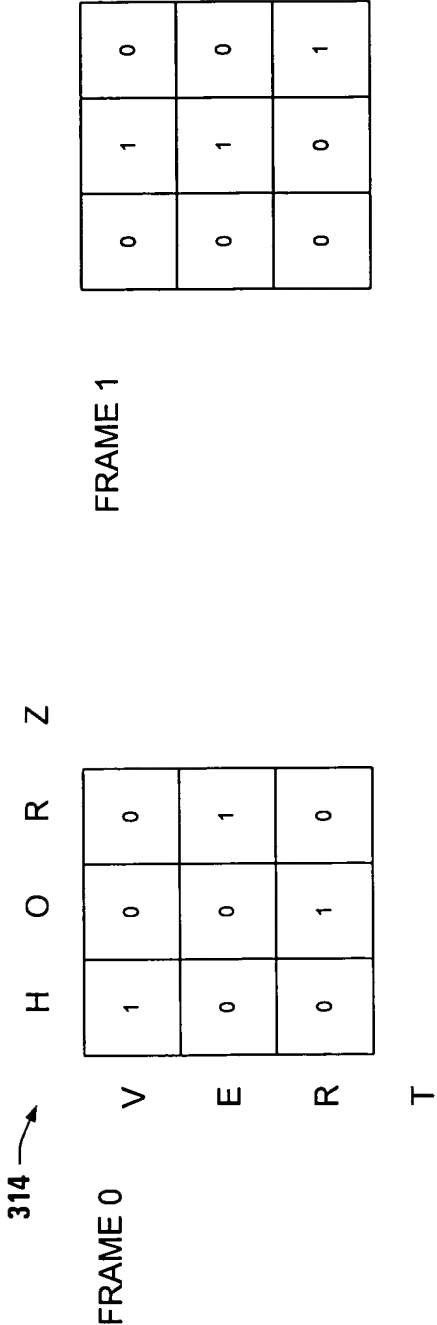


FIG. 27

[illegible]

316 ↗

FIG. 28

1 2 3 4

318 ↗

IRON

FRAME 0

1	0	0	0
0	0	1	0
0	1	0	0
0	0	0	1

FRAME 1

0	1	0	0
0	1	0	0
0	0	1	1

FRAME 2

0	0	1	1
1	0	0	1
1	0	0	0

FIG. 29

Display Type	Horizontal Resolution x Vertical Resolution	Video Clock frequency (MHz)	Frame Buffer Storage format	Display Data format	pixels per shift clock	Pixel Shift Clock frequency (MHz)	Vertical Frame Rate (Hz)
VFD	128 x 32	2	4 bpp	monochrome	8	0.25	400
LCD	128 x 64	2	4 bpp	monochrome	4	0.5	230
LCD	256 x 128	2	4 bpp	monochrome	4	0.5	60
"QVGA" TFT LCD	320 x 234	6.4	8 bpp	analog	1	6.4	80
QVGA STN LCD	320 x 240	4	4 bit RGB	4 bit RGB	1	4	50
HVGA STN LCD	640 x 240	8	4 bit RGB	4 bit RGB	1	8	50
"VGA" DC Plasma	640 x 400	16	4 bpp	monochrome	4	4	60
VGA EL	640 x 480	24	4 or 8 bpp	grayscale	8	3	75
VGA STN LCD	640 x 480	24	8 or 16 bpp	18 bit RGB	1	24	75
VGATFT LCD	640 x 480	24	8, 16, or 24 bpp	18 bit RGB	1	24	75
VGA CRT	640 x 480	25,175	8, 16, or 24 bpp	analog	1	NA	70
VGA CRT	640 x 480	32	8, 16, or 24 bpp	analog	1	NA	85
SVGA TFT LCD	800 x 600	40	8, 16, or 24 bpp	18 bit RGB	1	40	80
SVGA CRT	800 x 600	50	8, 16, or 24 bpp	analog	1	NA	85
XGA TFT LCD	1024 x 768	60	8, 16, or 24 bpp	18 bit RGB	2	30	72
XGA CRT	1024 x 768	75	8, 16, or 24 bpp	analog	1	NA	80
SXGA TFT LCD	1280 x 1024	85	8, 16, or 24 bpp	18 or 24 bit RGB	1	85	60
SXGA CRT	1280 x 1024	110	8, 16, or 24 bpp	analog	1	NA	70
SXGAW TFT LCD	1400 x 1024	90	8, 16, or 24 bpp	18 or 24 bit RGB	1	90	60
SXGA+ TFT LCD	1400 x 1050	110	8, 16, or 24 bpp	18 or 24 bit RGB	1	110	70
UXGA TFT LCD	1600 x 1200	135	8, 16, or 24 bpp	18 or 24 bit RGB	1	135	65
UXGA CRT	1600 x 1200	135	8, 16, or 24 bpp	analog	1	NA	60
UXGAW TFT LCD	1900 x 1200	135	8, 16, or 24 bpp	18 or 24 bit RGB	1	135	60
HDTV-2 LCD	1280 x 720	50	8, 16, or 24 bpp	24 bit RGB	1	50	50
HDTV-2 CRT	1280 x 720	66	8, 16, or 24 bpp	analog	1	NA	60
HDTV-4 LCD	1920 x 1080	135	8, 16, or 24 bpp	24 bit RGB	1	135	60
HDTV-4 CRT	1920 x 1080	135	8, 16, or 24 bpp	analog	1	NA	55
QXGA LCD	2048 x 1536	135	4 bpp	monochrome	8	16.875	40
QSXGA LCD	2560 x 2048	135	4 bpp	monochrome	8	16.875	24
QUXGA LCD	3200 x 2400	135	4 bpp	monochrome	8	16.875	17

FIG. 31

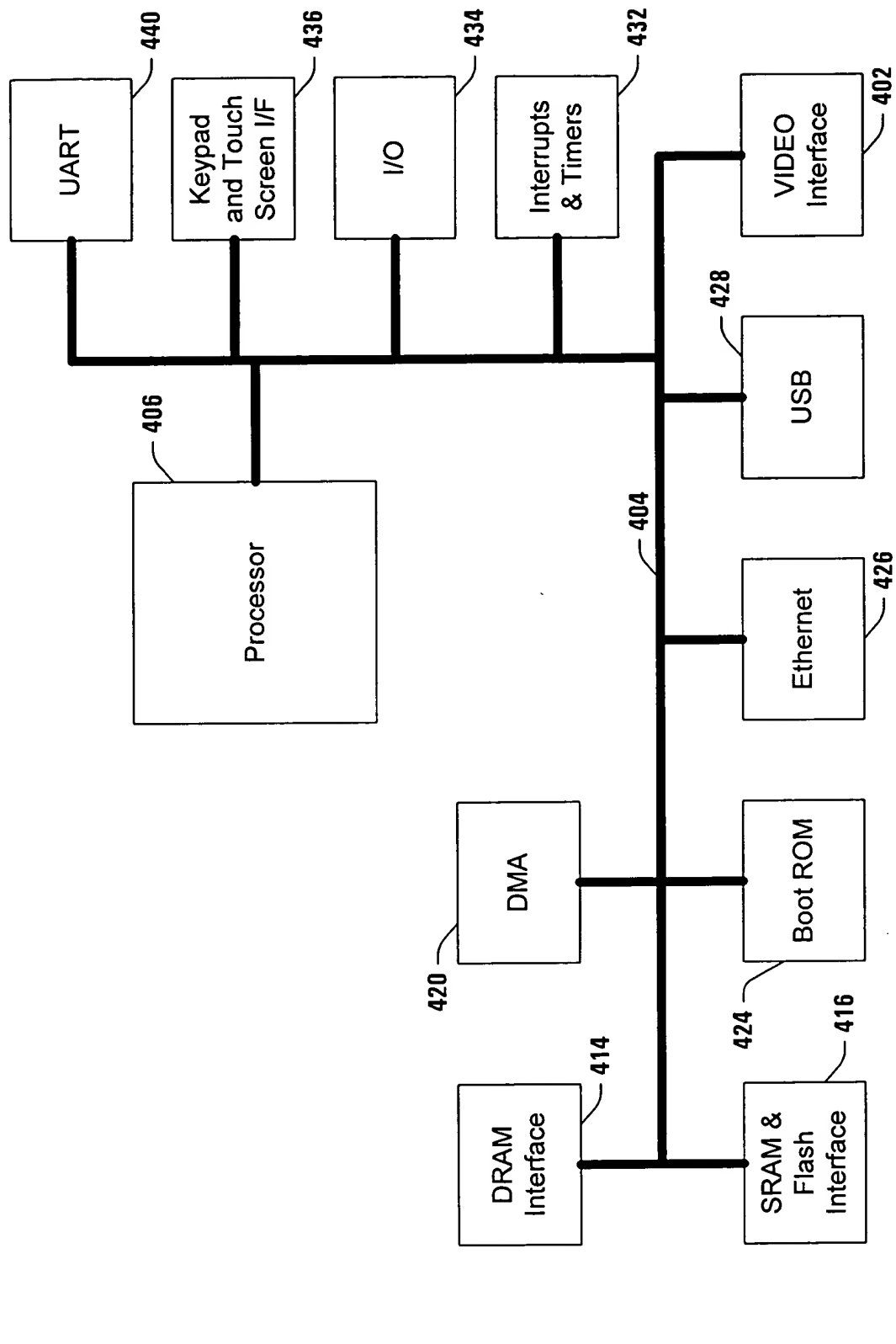


FIG. 32